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HIGH EFFICIENCY INVERTER AND BALLAST CIRCUITS

ABSTRACT OF THE DISCLOSURE

A high efficiency push-pull inverter circuit employing a pair of relatively high power switching transistors is described. The switching on and off of the transistors is precisely controlled to minimize power losses due to common-mode conduction or due to transient conditions that occur in the process of turning a transistor on or off. Two current feed-back transformers are employed in the transistor base drives; one being saturable for providing a positive feed-back, and the other being non-saturable for providing a subtractive feed-back.

CROSS REFERENCE TO RELATED APPLICATION.

This invention is a Continuation-In-Part of my application entitled "High Efficiency Push-Pull Inverters", filed March 20, 1978, Serial Number 890,586.

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